Applicant : Frank Preiss
Serial No. : 09/660,882
Filed : September 13, 2000
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Attorney's Docket No.: 12754-
120001 / 1999P07765US01

## REMARKS

Claims 1-2, 4, 6, 8, 10 and 12 have been amended. Claims 3, 7 and 11 have been cancelled. Claim 18 is newly submitted. Claims 1-2, 4-6, 8-10 and 12-18 are pending in the application. No new matter has been added. Reconsideration is respectfully requested in view of the amendments to the claims and the following remarks.

Claims 1-2 and 5 were rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,574,213 ("Anandakumar").

Claim 3 was rejected under 35 U.S.C. § 103(a) as being unpatentable over Anandakumar.

Claims 4, 6-9 and 10-15 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Anandakumar in further view of U.S. Patent No. 6,526,131 ("Zimmerman").

Claims 16-17 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Anandakumar in view of Zimmerman and U.S. Patent No. 6,449,269 ("Edholm").

Claim 1, as amended, recites one or more IEEE 802.3 media access controllers (MACs) that are integrated onto a same chip as a Voice-over-Internet Protocol processor core.

As acknowledged by the Examiner, Anandakumar fails to disclose that one or more of the communications ports of Anandakumar's processor are IEEE 802.3 MACs. The Examiner, however, suggests that it would have been obvious to one of ordinary skill in the art that one of the on-chip peripheral serial ports of Anandakumar's processor is an IEEE 802.3 compliant MAC port.

Even assuming that Anandakumar's processor includes a <u>MAC port</u>, Anandakumar fails to disclose one or more <u>MACs</u> integrated onto a same chip as a Voice-over-Internet Protocol processor core (emphasis added).

Rather, Anandakumar teaches away from integrating one or more MACs onto a same chip as a Voice-over-Internet Protocol processor core. While Anandakumar discloses a microcontroller (MCU) 1781 that includes stack 1811 having a MAC (col. 39, ll. 17-19; FIGs. 17-18), MCU 1781 and stack 1811 are not integrated onto a same chip as DSP IC 1511 (shown in FIG. 15). "A prior art reference must be considered in its entirety, i.e., as a whole, including portions that would lead away from the claimed invention." *W&L Gore & Associates, Inc. v.*

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*Garlock, Inc.*, 721 F.2d 1540, 220 USPQ 303 (Fed. Cir. 1983). Anandakumar, therefore, cannot render claim 1 obvious.

As with Anandakumar, neither Zimmerman nor Edholm discloses one or more MACs integrated onto a same chip as a Voice-over-Internet Protocol processor core as required by claim 1.

Claims 2 and 4-5 depend from claim 1 and are allowable for at least the same reason as set forth with respect to claim 1.
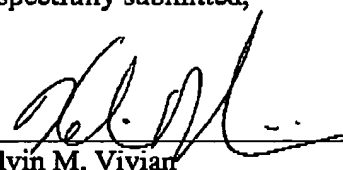
Claims 6 and 10, and the claims that depend therefrom, include limitations similar to claim 1 and, therefore, are allowable over each of Anandakumar, Zimmerman and Edholm, either alone or in combination, for similar reasons as set forth with respect to claim 1.

The applicant respectfully submits that the claims are in condition for allowance.

Please apply any other charges or credits to deposit account 06-1050.

Respectfully submitted,

Date: 09-23-04

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